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» Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

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IEEE STD IEEE Standard

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 Choi, Y.-H.; Malek, M.;
[Computers, IEEE Transactions on](#)
 Volume 37, Issue 5, May 1988 Page(s):621 - 624
 Digital Object Identifier 10.1109/12.4615
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- ☐ 2. **Fault tolerant arithmetic with applications in nanotechnology based syst**
 Wenjing Rao; Orailoglu, A.; Karri, R.;
[Test Conference, 2004. Proceedings. ITC 2004. International](#)
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- ☐ 3. **CRC-16 polynomials optimized for applications using self-synchronous s**
 Gorshe, S.S.;
[Communications, 2002. ICC 2002. IEEE International Conference on](#)
 Volume 5, 28 April-2 May 2002 Page(s):2791 - 2795 vol.5
 Digital Object Identifier 10.1109/ICC.2002.997351
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 Lettieri, A.; Holtz, K.; Holtz, E.;
[WESCON/94. 'Idea/Microelectronics'. Conference Record](#)
 27-29 Sept. 1994 Page(s):398 - 403
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"ERROR CHECKING CODE" "M-BITS" CALC

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Did you mean: "**ERROR CHECKING CODE**" "**MBITS**" **CALCULATING**

Method and means for discriminating between systematic and noise ...

This is accomplished by **calculating** the value for each one of the log.sub.2 (m) **bits** as a function of the data bits in counterpart bytes in each word of ...

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Method and means for discriminating between systematic and noise ...

As previously pointed out, each memory word is **m bits** in length. ... This is accomplished by **calculating** the value for each one of the log.sub.2 (m) **bits** as ...

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Data processing system and method patent invention

The first and second partial results are used to **calculate** the overall CRC ... least one **error checking code**, comprising **m-bits**; **calculating** an intermediate ...

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Data processing system and method patent invention

A method for generating at least one **error checking code** , comprising : creating a ... **calculating** an intermediate m - bit **error checking code** using a first ...

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[PDF] Quantum Information Authentication Using Entangled States

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to exchange **m bits** of classical information, they just do as. follows. ... They may be solved by **error-checking code** of the informa- tion being sent. ...

doi.ieeecomputersociety.org/10.1109/ICDT.2006.66 - [Similar pages](#)

Data processing system and method - IP.com's Patent Debate

Preferably, embodiments provide a method in which the step of **calculating** the at least one **error checking code** by combining the intermediate m-bit **error** ...

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[PDF] Automatisierungssystem S7-200

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(**M bits**) for the TD 200 to set when a function key is pressed. ... The wizard will also allow you to attach **error-checking code** to the output, so that ...

robotics.dem.uc.pt/norberto/cas0102/s7200.pdf - [Similar pages](#)

Cyclic redundancy checks

Calculating a Cyclic Redundancy Check is a much more robust error ... A Cyclic Redundancy Check or CRC value of length **M bits** is designed with ...

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☐	L12	((("4138676" "7020123")!.PN.) .p64-p143.	2
		<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>	
☐	L11	error with check\$3 with cod\$3 WITH (PRIOR OR PREVIOUS)same (FIRST or LOW OR HIGH OR SECOND) with (part or portion or partition).CLM.	3
☐	L10	L9 AND error with check\$3 with cod\$3 WITH (PRIOR OR PREVIOUS)same (FIRST or LOW OR HIGH OR SECOND) with (part or portion or partition) and (galois or finite)	0
☐	L9	error with check\$3 with cod\$3 same (FIRST or LOW OR HIGH OR SECOND OR PRIOR OR PREVIOUS) with (part or portion or partition) and (galois or finite)	77
☐	L8	L7 and @pd > 20060514	0
☐	L7	('5428629')!.ABPN1,NRPN,PN,TBAN,WKU.	2
☐	L6	('5719884')!.ABPN1,NRPN,PN,TBAN,WKU.	2
☐	L5	('6721919')!.ABPN1,NRPN,PN,TBAN,WKU.	2
☐	L4	('5428629')!.ABPN1,NRPN,PN,TBAN,WKU.	2
☐	L3	('7028248')!.ABPN1,NRPN,PN,TBAN,WKU.	2
☐	L2	('20050086571')!.ABPN1,NRPN,PN,TBAN,WKU.	2
☐	L1	error with check\$3 with cod\$3 same (intermediate or interim) with (part or portion or partition) and (galois or finite)	9

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